

REMARKS

All of the original claims 1-8 have been cancelled, and replaced with claims 9-28 to ensure that the Applicant sets forth with particularity what the Applicant regards as his invention.

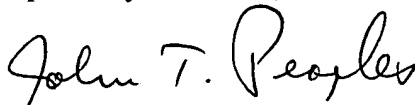
Accordingly, substantially all of the Summary of Invention (pages 8 and 9) has been replaced to be commensurate with the newly-added claims.

Also, the Abstract of the Disclosure has been replaced so as to describe the contents of the application with specificity.

Typographical errors on page 13 relating to Figure numbers have been amended to secure correspondence between the Figures and the specification.

Finally, various typographical errors in the specification have been corrected.

Respectfully submitted,



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VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the Specification:

Page 13, line 1 has been amended as follows: --FIG. 21B depicts a (1 2 3) permutation on an 8×8 exchange;--

Page 13, line 2 has been amended as follows: --FIG. 21C depicts a (3 1) permutation on an 8×8 exchange;--

Page 13, line 3 has been amended as follows: --FIG. 21D depicts a combined (1 4)(2 3) permutation on an 8×8 exchange;--

Page 215, line 5 has been amended as follows: --cells--.

Page 218, line 9 has been amended as follows: --surface 7407 of a rectangular interface board, and the output switching elements are--.

Page 219, line 2 has been amended as follows: --realization of a “fiber-array package”. Each I/O switching element in a fiber-array--.

Page 220, added after line 1:--Once a PCB is resulted from a step of PCB implementation, it cannot be used in another step of PCB implementation, and the same for the IC chip.--.

Page 221, line 9 has been amended as follows: -- construction and is not implemented in any of the aforementioned levels. Such a--.

Page 223, line 14 has been amended as follows: -- interconnection network associated with the sub-tree rooted at the internal node 20071. The--.